PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Yuji YAMASAKI, et al.

Confirmation Number: 4712

Serial No.: 10/734,296

Group Art Unit: 2811

Filed: December 15, 2003

Examiner:

For:

SEMICONDUCTOR MEMORY DEVICE AND SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of National Publication of Translated Version No. 2002-522871, is discussed in the present specification.

10/734,296

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: April 15, 2004

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INFORMATION DISCLOSURE CITATION IN AN					ATTY. DOCKET NO. 60188-733		SERIAL NO. 10/734,296			
APPLICATION										
					APPLICANT Yuji YAMASAKI, e	APPLICANT Yuji YAMASAKI, et al.				
(PTO-1449)					FILING DATE December 15, 2003		GROUP 2811			
				U.S. PATEN	NT DOCUMENTS				. "	
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